DHAMMPAL RAMTAKE

ASSISTANT PROFESSOR (CONTRACT), S.O.S IN COMPUTER SCIENCE & IT, PT. RAVISHANKAR SHUKLA UNIVERSITY, RAIPUR, CHHATTISGARH Email id.: dhammpal88@gmail.com

EDUCATIONAL QUALIFICATION

<u>I have cleared UGC NET November 2017, CGSET 2017 Examination for Assistant Professor and</u>

also completed my CCNA and CCNP training at ZOOM Technology Hyderabad (AP). I have completed Master of Philosophy from S.o.S in Computer Sciences & IT, Pt.RSU, Raipur (C.G) with 66.6% aggregate. Also pursuing Doctorate of Philosophy from SoS in Computer sciences & IT, Pt.RSU Raipur (C.G).

Examination	Degree	Institute/School	University/Board	Year of Passing
Ph.D.(CS)	Ph.D.	SoS in Computer science & IT	Pt.RSU Raipur	17-11-15 to till
M.Phil.(CS)	M.Phil.	SoS in Computer science & IT	Pt.RSU Raipur	2013
МСА	MCA	BIT Mesra	BIT Mesra Ranchi	2012
BCA	BCA	RCIT Durg	MCRPV Bhopal	2009
12 th		Govt. H.S School Bhilai	C.G. board of hr. sec. education	2006
10 th		Govt. H.S School Bhilai	C.G. board of hr. sec. education	2004

Journal/Conferences/Workshop

S. No	Paper Title	Journal/Conferences/Workshop	Year
1	POWER CONSUPTION OF ROUTING PROTOCOALS IN VARIOUS SIMULATORS: MANET	National Conference on Education and Research Scenario of Mathematics and Computer Science SPASM Rajim (C.G)	Jan 2012
2	GRID SCHEDULING ALGORITHMS TOACHIEVES EFFECTIVE TASK ALLOCATION	National Conference on High Performance Computing & Simulation NIST Berhampur ODISHA	Jan 2012
3	TSP BY CONTROLING PARAMETER OF AN ANT COLONY OPTIMIZTION: A GREEN APPROCH	National Conference on Recent Trend Green Computing SoS in CS & IT, Pt.RSU Raipur	Oct 2013
4	IMPACT OF CACHE MEMORY IN MULTICORE SYSTEM PERFORMANCE	National Conference on Innovative Trends in Computer Science & Information Technology, Raipur	2014
5	GPU COMPUTING PROGRAMMING AND APPLICATIONS	GPA 2, IIT Madras	2014
6	HIGH PERFORMANCE COMPUTING	WHPC 2015, ISM Dhanbad	2015

7	ROUTE OPTIMSATION BY ANT COLONY OPTIMISATION	2 nd International Conference on Intelligent Computing , Communication & Convergence , Bhubaneswar	2016
8	PERFORMANCE ANALYSIS OF FIRST LEVEL CACHE MEMORY REPLACEMENT POLICIES IN MULTICORE SYSTEMS	International Conference on Advances In Computing Applications (ICACA- 2018), NIT Uttarakhand	Feb 2018

TECHNICAL SKILLS

Languages known	: C, C++, Core java.	
Packages	: Office- 2003, 2007, Oracle-9i.VB.net.	
Operating systems	: WINDOWS 7, 8, Linux.	
Other	: Networking, Data Structure, Parallel Computing, MATLAB, Soft Computing.	

EXPERIENCE

- 1. Assistant Professor (Contract) at SoS in Computer Science and IT in Pt. Ravishankar Shkula University Raipur Chhattisgarh. Duration 1st September 2014 to 30th April 2015.
- 2. Assistant Professor (Contract) at GOVT. NPG College of Science, Raipur, Chhattisgarh 3rd September 2015 to 30 April 2016.
- 3. Assistant Professor (Contract) at GOVT. NPG College of Science, Raipur, Chhattisgarh 18 August 2016 to 30 April 2017.
- 4. **Assistant Professor** (Contract) at GOVT. NPG College of Science, Raipur, Chhattisgarh 30 August 2017 to 28 February 2018.
- 5. **Assistant Professor** (Contract) at SoS in Computer Science and IT in Pt. Ravishankar Shkula University Raipur Chhattisgarh. Duration 28th August 2018 to 30th April 2019.

ACHIEVEMENTS & CO-CURRICULUM ACTIVITIES

- 1) First prize on science exhibition at inter school zonal level competition.
- 2) Second prize at inter school debate competition.
- 3) Selected in MHCET and BIT MESRA Entrance for MCA course in 2009.
- 4) Local Organizing Committee member in National Conference 2013(NCON 2013).
- 5) Staff Coordinator for organizing national Workshop on ANDRIOD 2014.